Bounded Version Vectors

José Bacelar Almeida [*], Paulo Sérgio Almeida, and Carlos Baquero

Departamento de Informática, Universidade do Minho
{jba,psa,cbm}@di.uminho.pt

Abstract. Version vectors play a central role in update tracking under optimistic distributed systems, allowing the detection of obsolete or inconsistent versions of replicated data. Version vectors do not have a bounded representation; they are based on integer counters that grow indefinitely as updates occur. Existing approaches to this problem are scarce; the mechanisms proposed are either unbounded or operate only under specific settings. This paper examines version vectors as a mechanism for data causality tracking and clarifies their role with respect to vector clocks. Then, it introduces bounded stamps and proves them to be a correct alternative to integer counters in version vectors. The resulting mechanism, bounded version vectors, represents the first bounded solution to data causality tracking between replicas subject to local updates and pairwise symmetrical synchronization.

Keywords: Replication, causality, version vectors, update tracking, bounded state.

1 Introduction

Optimistic replication is a critical technology in distributed systems, in particular when improving availability of database systems and adding support to mobility and partitioned operation [18]. Under optimistic replication, data replicas can evolve autonomously by incorporation new updates into their state. Thus, when contact can be established between two or more replicas, mutual consistency must be evaluated and potential divergence detected.

The classic mechanism for assessing divergence between mutable replicas is provided by *version vectors* which, since their introduction by Parker et al. [14], have been one of the cornerstones of optimistic data management. Version vectors associate to each replica a vector of integer counters that keeps track of the last update that is known to have been originated in every other replica and in the replica itself. The mechanism is simple and intuitive but requires a state of unbounded size, since each counter in the vector can grow indefinitely.

The potential existence of a bounded substitute to version vectors has been overlooked by the community. A possible cause is a frequent confusion of the roles played by *version vectors* and *vector clocks* (e.g. [17, 18]), that have the same representation

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[14, 5, 13], together with the existence of a minimality result by Charron-Bost [4], stating that vector clocks are the most concise characterization of causality among process events.

In this article we show that a bounded solution is possible for the problem addressed by version vectors: the detection of mutual inconsistency between replicas subject to local updates and pairwise symmetrical synchronization. We present a mechanism, *bounded stamps*, that can be used to replace integer counters in version vectors, stressing that the minimality result that precludes bounded vector clocks does not apply to version vectors.

1.1   On version vectors and vector clocks

Asynchronous distributed systems track causality and logical time among communicating processes by means of several mechanisms [12, 19], in particular vector clocks [5, 13].

While being structurally equivalent to version vectors, vector clocks serve a very distinct purpose. Vector clocks track causality by establishing a strict partial order on the events of processes that communicate by message passing, and are known to be the most concise solution to this problem. Vector clocks, being a vector of integer counters, are unbounded in size, but so is the number of events that must be ordered and timestamped by them. In short, *vector clocks order an unlimited number of events occurring in a given number of processes*.

If we consider the role of version vectors, data causality, there is always a limit to the number of possible relations that can be established on the set of replicas. This limit is independent on the number of update events that are considered on any given run. For example, in a two replica system $\{r_a, r_b\}$ only four cases can occur: $r_a = r_b$, $r_a < r_b$, $r_b > r_a$ and $r_a \parallel r_b$. If the two replicas are already divergent the inclusion of *new* update events on any of the replicas does not change their mutual divergence and the corresponding relation between them. In short, *version vectors order a given number of replicas, according to an unlimited number of update events*.

The existence of a limited number of relations is a necessary but not sufficient condition for the existence of a bounded characterization mechanism. A relation, which is a global abstraction, must be encoded and computed through local operations on replica pairs without the need for a global view. This is one of the important properties of version vectors.

2   Data causality and version vectors

Data causality on a set of replicas can be assessed via set inclusion of the sets of update events known to each replica. Data causality is the pre-order defined by:

$$r_a \leq r_b \quad \text{iff} \quad U_a \subseteq U_b$$

being U_a and U_b the sets of update events (globally unique events), known to replicas r_a and r_b.
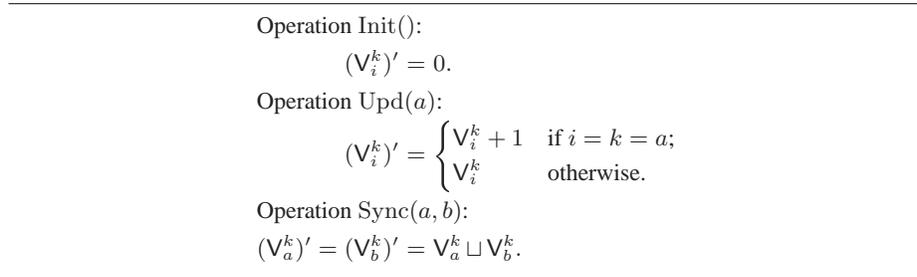
Fig. 1. Semantics of version vector operations.

When tracking data causality with version vectors in an N replica system, one associates to each replica $r_i \in \{r_0, \ldots, r_{N-1}\}$ a stamp V_i which is a vector of N integer counters. The order on version vectors is the standard pointwise (coordinatewise) order:

$$\mathsf{V}_a \leq_\mathsf{V} \mathsf{V}_b \quad \text{iff} \quad \forall k.\, \mathsf{V}_a^k \leq \mathsf{V}_b^k$$

where V_i^k denotes component k of vector V_i.

The operations on version vectors, formally presented in Fig. 1, are as follows:

Initialization (Init()) establishes the initial system state. All vectors are initialized with zeroes.

Update (Upd(a)) an update event in replica r_a increments V_a^a.

Synchronization (Sync(a, b)) synchronization of r_a and r_b is achieved by taking the pointwise join (greatest element) of V_a and V_b.

This classic mechanism encodes data causality because comparing version vectors gives the same result as comparing sets of known update events. For all runs and replicas r_a and r_b:

$$r_a \leq r_b \quad \text{iff} \quad U_a \subseteq U_b \quad \text{iff} \quad \mathsf{V}_a \leq_\mathsf{V} \mathsf{V}_b.$$
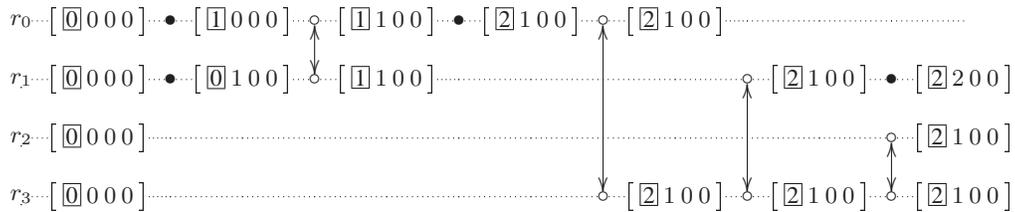
Fig. 2. Version Vectors: example run, depicting slice 0 counters by a boxed digit.

Figure 2 shows a run with version vectors in a four replica system. Updates are depicted by a "•" and synchronization by two "○" connected by a line.

Operation $\mathrm{Init}()$:
$$(\mathsf{S}_i)' = 0.$$

Operation $\mathrm{Upd}(0)$:
$$(\mathsf{S}_i)' = \begin{cases} \mathsf{S}_i + 1 & \text{if } i = 0; \\ \mathsf{S}_i & \text{otherwise.} \end{cases}$$

Operation $\mathrm{Sync}(a, b)$:
$$(\mathsf{S}_a)' = (\mathsf{S}_b)' = \mathsf{S}_a \sqcup \mathsf{S}_b.$$

Fig. 3. VVS semantics for slice 0.

2.1 Version vector slices

All operations over version vectors exhibit a pointwise nature: a given vector position is only compared or updated to the same position in other vectors, resulting from all information about updates originated in replica r_k being stored in component k of each version vector. This allows a decomposition of the replicated system into N *slices*, where each slice represents the updates that were originated in a given replica. Slice i for a N replica system is made up of the ith component of each version vector:

$$\langle \mathsf{V}_0^i, \ldots, \mathsf{V}_{N-1}^i \rangle.$$

This means that data causality in N replicas can be encoded by the concatenation of the representation for each of the N slices. It also means that it is enough to concentrate on a subproblem: encoding the distributed knowledge about a single source of updates, and the corresponding version vector slice (VVS). The source of updates increments its counter and all other replicas keep potentially outdated copies of that counter; this subproblem amounts to storing a distributed representation of a total order.

For the remainder of the paper we will concentrate, for notational convenience and without loss of generality, on finding a bounded representation for slice 0. Figure 3 presents the semantics of version vectors restricted to slice 0; in the run presented in Fig. 2 this slice is shown using boxed counters.

3 Informal presentation

We now give an informal presentation of the mechanism and give some intuition of how it works and how it accomplishes its purpose. Having shown that it is enough to concentrate on a subproblem (a single source of updates) and the corresponding slice of version vectors, we now present the stamp that will replace, in each replica, the integer counter of the corresponding version vector.

For problem size N, i.e. assuming N replicas, with r_0 the "primary" where updates take place and $r_1, \ldots, r_{N-1}$ the "secondary" replicas, we represent a stamp by something like

c	b	a
c	a	
a		
c	a	

Fig. 4. Bounded stamps: example run.

It has a representation of bounded size, as it consists of N rows, each with at most N symbols (letters here), taken from a finite set $\mathcal{L}_N$. An example run consisting of four replicas is presented in Fig. 4.

A stamp is, in abstract, a vector of totally ordered sets. Each of the N components (rows in our notation) represents a total order, with the greatest element on the left (the first row above means $c > b > a$). In a stamp for replica r_i, row i ($i \in \{0, \dots N-1\}$) is what we call the *principal order* (displayed with a gray background), while the other rows are the *cached orders*. (Thus, the stamp above would belong to replica r_3.) The cached order in row j represents the principal order of replica j at some point in time, propagated to replica i (either directly or indirectly through several synchronizations).

The greatest element of the principal order (on the left, depicted in bold over gray) is what we call the *principal element*. It represents the most recent update (in the primary) known by the replica. In a representation using an infinite total ordered set instead of $\mathcal{L}_N$ nothing more would be needed. This element can be thought of as "corresponding" to the value of the integer counter in version vectors.

The left column in a stamp (depicted in bold) is what we call the *principal vector*; it is made up of the greatest element of each order (row). It represents the most recent local knowledge about the principal element of each replica (including itself).

In a stamp, there is a relationship between the principal order and the principal vector: the elements in the principal vector are the same ones as in the principal order. In other words, the set of elements in the principal vector is ordered according to the principal order.

3.1 Comparison and synchronization as well defined local operations

As we will show below, the mechanism is able to compare two stamps by a local operation on the respective principal orders. No global knowledge is used: not even a global order on the set of symbols $\mathcal{L}_N$ is assumed. For comparison purposes $\mathcal{L}_N$ is simply an unordered set, with elements that are ordered differently in different stamps. As an example, the comparison of

$$
r_0 = \begin{array}{|ll|}
\hline
\mathbf{b}\ \text{c} \\
\mathbf{c}\ \text{a} \\
\mathbf{c} \\
\mathbf{c} \\
\hline
\end{array}
\quad \text{with} \quad
r_1 = \begin{array}{|lll|}
\hline
\mathbf{c}\ \text{b}\ \text{a} \\
\mathbf{c}\ \text{a} \\
\mathbf{a} \\
\mathbf{c}\ \text{a} \\
\hline
\end{array}
$$

involves looking at $\mathbf{b}$ c and $\mathbf{c}$ a , and gives $r_0 > r_1$.

When synchronizing two stamps, in the positions of the two principal elements, the resulting value will be the maximum of the two principal elements; the rest of the resulting principal vector will be the pointwise maximum of the respective values. The comparisons are performed according to the principal orders of the two stamps involved.

It is important to notice that, in general, it is not possible to take two arbitrary total orders and merge them into a new total order. As such, it could be thought that computing the maximum as mentioned above is ill defined. As we will show, several properties of the model can be exploited that make these operations indeed possible and well defined. We will also show that it is possible to totally order the elements in the resulting principal vector, i.e. to obtain a new principal order.

3.2 Garbage collection for symbol reuse

The boundedness of the mechanism is only possible through symbol reuse. When an update operation is performed, instead of incrementing an integer counter, some symbol is chosen to become the new principal element. By using a finite set of symbols $\mathcal{L}_N$, an update will eventually reuse a symbol that was already used in the past to represent some previous update that has been synchronized with other replicas.

However, by reusing symbols, an obvious problem arises that needs to be addressed: the symbol reuse cannot compromise the well-definedness of the comparison operations described above. As an example, it would not be acceptable that, due to reuse, the principal orders of two stamps end up being $\mathbf{a}$ b c and $\mathbf{c}$ a , as it would not be possible to overcome the ambiguity between $a > b > c$ and $c > a$ and to infer which one is the greatest stamp.

To address the problem, the mechanism implements a distributed "garbage collection" of symbols. This is accomplished through the extra information in the cached orders. As we will show, any element in the principal order/vector of any replica is also present in the primary replica (in some principal or cached order). This is the key property towards symbol reuse: when an update is performed, any symbol which is not present in the primary replica is considered "garbage" and can be (re)used for the new principal element.

As an example, in Fig. 4, when the final update occurs, symbol b can be used for the new principal element because it is not present in the primary replica:

$$
\begin{array}{|l|}
\hline
\mathbf{c} \\
\mathbf{c}\ \text{a} \\
\mathbf{c} \\
\mathbf{c} \\
\hline
\end{array}
$$
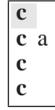
Notice that the scheme only assures that b does not occur in the principal orders/vectors. In this example b occurs in some cached orders of replicas

$$
r_1 = \begin{array}{|l|}
\hline
\mathbf{c}\ \text{b}\ \text{a} \\
\mathbf{c}\ \text{a} \\
\mathbf{a} \\
\mathbf{c}\ \text{a} \\
\hline
\end{array}
\quad \text{and} \quad
r_2 = \begin{array}{|l|}
\hline
\mathbf{c}\ \text{b}\ \text{a} \\
\mathbf{c}\ \text{a} \\
\mathbf{c} \\
\mathbf{c} \\
\hline
\end{array}
$$
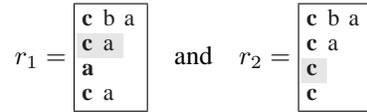
but this is not a problem because those elements will not be used in comparisons; the "old" b will not be confused with the "new" b.

3.3 Synopsis of formal presentation

The formal presentation and proof of correctness will make use of an unbounded mechanism which we call the *counter mode principal vectors* (CMPV). This auxiliary mechanism represents what the evolution of the principal vector would be if we could afford to use integer counters. The mechanism makes use of the total order on natural numbers and does not encode orders locally. In Fig. 5 we present part of the run in Fig. 4 using the counter mode mechanism.

The bulk of the proof consists in establishing several properties of the CMPV model that allow the relevant comparison operations to be computed in a well-defined way using only local information. The key idea is that, exploiting these properties, bounded stamps can be seen as an encoding of CMPV using a finite set $\mathcal{L}_N$, where the principal orders are used to encode the relevant order information.

4   Counter Mode Principal Vectors

Version Vector Slices (VVS) rely on an unbounded totally ordered set — the natural numbers. Their unbounded nature is actually a consequence of adopting a predetermined order relation (and hence globally known) to capture data causality among replicas. To overcome this, we enrich VVS in a way that order judgments become, in a sense, local to each replica. In this way, it will be possible to dynamically encode the causality order and open the perspective of bounding the "counters" domain.

For a replica index a, its stamp in the CMPV model is denoted by C_a and defined as the tuple $\langle a, \mathsf{a} \rangle$ where a is a vector of integers with size N — the *principal vector* for C_a (see Fig. 5). The value in position k of vector a is denoted by a^k and represents the knowledge of stamp C_a concerning the most recent update known by stamp C_k. The element a^a plays a central role since it holds C_a's view about the more recent update — this is essentially the information contained in VVS counters and we call it the *principal element* for stamp C_a.
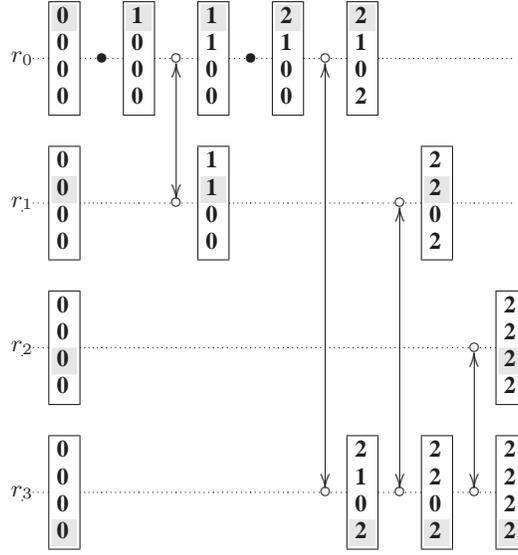
Fig. 5. Counter mode principal vectors.

Figure 6 defines the semantics of the operations in the CMPV model. Symbol $\sqcup$ denotes the join operation under integer ordering (i.e. taking the maximum element). Notice that the order information is only required to perform the synchronization operation. Moreover, comparisons are always between principal elements or pointwise (between the same position in two principal vectors). Occasionally, it will be convenient to write $a \sqcup b$ for the result of the synchronization on stamps C_a and C_b (i.e. the principal vector of one of these stamps after synchronization).

A *trace* consists of a sequence of operations starting with $\mathrm{Init}()$ and followed by an arbitrary number of updates and synchronizations. In the remainder, when stating properties in the CMPV, we will leave implicit that they only refer to reachable states, i.e. states that result from some trace of operations. Induction over the traces is the fundamental tool to prove invariance properties, as the following simple facts about CMPV.

Proposition 1. *For every replica C_a, C_b and index k,*

1. $a^b \leq b^b$,
2. $a^a \leq 0^0$,
3. $a^k \leq a^a$.

Proof. Simple induction on the length of traces.

Given stamps C_a and C_b we define their *data causality order under CMPV* ($\leq_C$) as the comparison of their principal elements:

$$C_a \leq_C C_b \quad \text{iff} \quad a^a \leq b^b.$$

Operation $\mathrm{Init}()$:
$$(\mathsf{a}^k)' = 0.$$

Operation $\mathrm{Upd}(0)$:
$$(\mathsf{a}^k)' = \begin{cases} \mathsf{a}^k + 1 & \text{if } a = k = 0; \\ \mathsf{a}^k & \text{otherwise.} \end{cases}$$

Operation $\mathrm{Sync}(a, b)$:
$$(\mathsf{a}^k)' = (\mathsf{b}^k)' = \begin{cases} \mathsf{a}^a \sqcup \mathsf{b}^b & \text{if } k \in \{a, b\}; \\ \mathsf{a}^k \sqcup \mathsf{b}^k & \text{otherwise.} \end{cases}$$

Fig. 6. Semantics of operations in CMPV.

By Fig. 6 it can be seen that the computation of principal elements only depends upon principal elements. Moreover, if we restrict the impact of the operations to the principal element we recover the VVS semantics (Figure 3). This observation leads immediately to the correctness of CMPV as a data causality encoding for slice 0:

$$\mathsf{C}_a \leq_\mathsf{C} \mathsf{C}_b \quad \text{iff} \quad \mathsf{V}_a^0 \leq_\mathsf{V} \mathsf{V}_b^0.$$

This result is not surprising since CMPV was defined as a semantics preserving extension of VVS.

Next we will show that the additional information contained in the CMPV model makes it possible to avoid relying on the integer order, and to replace it with a locally encoded order. For this, we will use a non-trivial invariant on the global state given by the following lemma.

Lemma 1. *For every stamp C_a and C_b and index k,*

$$\mathsf{a}^a \leq \mathsf{b}^b \; \text{and} \; \mathsf{b}^k \leq \mathsf{a}^k \quad \text{implies} \quad \mathsf{a}^k \in \mathsf{b}.$$

Proof. See [1].

Recall that the order information is only required to perform the synchronization operation. Moreover, comparisons are always between principal elements or pointwise (between the same position in two principal vectors). In the following we will show that these comparisons can be performed without relying on integer order as long as we can order the elements in the principal vector of each stamp individually.

Comparison between principal elements reduces to a membership testing.

Proposition 2. *For every stamp C_a, C_b,*

$$\mathsf{a}^a \leq \mathsf{b}^b \quad \text{iff} \quad \mathsf{a}^a \in \mathsf{b}.$$

Proof. $\implies$ If $\mathsf{a}^a \leq \mathsf{b}^b$ then, by Proposition 1(1) we have that $\mathsf{b}^a \leq \mathsf{a}^a$ and so, by Lemma 1, $\mathsf{a}^a \in \mathsf{b}$.
$\impliedby$ If $\mathsf{a}^a \in \mathsf{b}$ then, by Proposition 1(3) we have that $\mathsf{a}^a \leq \mathsf{b}^b$.

For a stamp C_a, let us denote by $\leq^a$ the restriction of the intrinsic integer order to the values contained in the principal vector a:

$$x \leq^a y \quad \text{iff} \quad x \leq y \text{ and } x \in a \text{ and } y \in a.$$

Using these orderings, we define new ones that are appropriate to perform the required comparisons. For stamps C_a and C_b, let their combined order $\leq^{ab}$ be defined as:

$$x \leq^{ab} y \quad \text{iff} \quad (b^b \in a \text{ and } (x \in a \Rightarrow x \leq^a y)) \text{ or} \\ (a^a \in b \text{ and } (x \in b \Rightarrow x \leq^b y)).$$

For convenience, we also define the corresponding join operation $\underset{ab}{\sqcup}$ as:

$$x \underset{ab}{\sqcup} y = \begin{cases} y & \text{if } x \leq^{ab} y, \\ x & \text{otherwise.} \end{cases}$$

The following proposition establishes the claimed properties for this ordering.

Proposition 3. *For every stamp C_a and C_b and index k,*

1. $a^a \leq b^b$ *iff* $a^a \leq^{ab} b^b$,
2. $a^k \leq b^k$ *iff* $a^k \leq^{ab} b^k$.

Proof. (1) Follows directly from Propositions 1 and 2.

(2) $\Longrightarrow$ Let $a^k \leq b^k$. When $b^b \leq a^a$ Proposition 2 guarantees that $b^b \in a$ and, by Lemma 1, we have $b^k \in a$ and then $a^k \leq^a b^k$, which establishes $a^k \leq^{ab} b^k$. The case $a^a < b^b$ is trivial since, either $a^k \in b$ (in which case $a^k \leq^b b^k$), or $a^k \notin b$ and so $a^k \leq^{ab} b^k$. $\Longleftarrow$ Let $a^k \nleq b^k$ (that is, $b^k < a^k$). The proof proceeds as in the previous implication.

Restricted orders can be explicitly encoded (e.g. by a sequence) and can be easily manipulated. We now show that when a synchronization is performed, all the elements in the resulting principal vector were already present in the more up-to-date stamp. This means that the restricted order that results is a restriction of the one from the more up-to-date stamp.

Proposition 4. *Let C_a and C_b be stamps and $C_x = C_a \sqcup C_b$. If $a^a \leq b^b$ then, for all k,*

$$x^k \in b.$$

Proof. For the pointwise join $x^k = a^k \sqcup b^k$: if $a^k \leq b^k$ then $x^k = b^k \in b$; if $b^k \leq a^k$ then, by Lemma 1, $a^k \in b$. Otherwise, note that the resulting principal element (b^b) is already in b.

These observations together with the fact that the global state can only retain a bounded amount of integer values (an obvious limit is N^2) opens the way for a change in the domain from the integers in the CMPV model to a finite set.

5 Bounded Stamps

A migration from the domain of integer counters in CMPV to a finite set $\mathcal{L}_N$ is faced with the following difficulty: the update operation should be able to choose a value, that is not present in any principal vector, for the new principal element in the primary.

Adopting a set $\mathcal{L}_N$ sufficiently large (e.g. with N^2 elements) guarantees that such a choice exists under a global view. The problem lies in making that choice using only the information in the state of the primary. To overcome this problem we make a new extension of the model that allows the primary to keep track of all the values in use in the principal vectors of all stamps.

We will present this new model parameterized by a set $\mathcal{L}_N$ (the symbol domain), a distinguished element $\mathbf{0} \in \mathcal{L}_N$ (the initial element), and an oracle for new symbols $\text{new}(-)$ (satisfying an axiom described below). For each replica index a, its local state in the bounded stamps model is denoted by B_a and defined as $\langle a, \underline{a}, \overline{\underline{a}} \rangle$ where:

- a is the replica index;
- $\underline{a}$ is a vector of values from $\mathcal{L}_N$ with size N — the principal vector;
- $\overline{\underline{a}}$ is a vector of N total orders, encoded as sequences, representing the full bounded stamp.

This last component contains all the information in the principal vector, the principal order and the cached orders. Although the principle vector $\underline{a}$ is redundant (as each component $\underline{a}^k$ is also present in the first position of each $\overline{\underline{a}}^k$), it is kept in the model for notational convenience in describing the operations and in establishing the correspondence between the models.

The intuitive idea is that the state for each stamp keeps an explicit representation of the restricted orders. More precisely, for stamp B_a, the sequence $\overline{\underline{a}}^a$ contains precisely the elements of $\underline{a}$ ordered downward (first element is $\underline{a}^a$). From that sequence one easily defines the restricted order for stamp B_a, what we call *principal order* to emphasize its explicit nature.

$$x \leq_{\mathsf{B}}{}^a y \quad \text{iff} \quad x = y \text{ or } \langle y, x \rangle = \overline{\underline{a}}^a_{|\{x,y\}}$$

where $l_{|m}$ denotes the sequence l restricted to the elements in m, i.e. $\langle x \mid x \in l \text{ and } x \in m \rangle$. The combined order $\leq^{\mathsf{ab}}$ and associated join are defined precisely as in counter mode, that is

$$\begin{aligned} x \leq^{\mathsf{ab}} y \quad \text{iff} \quad & (\underline{b}^b \in \underline{a} \wedge (x \in \underline{a} \Rightarrow x \leq_{\mathsf{B}}{}^a y)) \text{ or} \\ & (\underline{a}^a \in \underline{b} \wedge (x \in \underline{b} \Rightarrow x \leq_{\mathsf{B}}{}^b y)). \end{aligned}$$

The other sequences in $\overline{\underline{a}}$ keep information about (potentially outdated) principal orders of other stamps — these are called the *cached orders*.

Figure 7 gives the semantics for the operations in this model. The oracle for new symbols $\text{new}(-)$ is a function that gives an element of $\mathcal{L}_N$ satisfying the following axiom:

$$\text{For every stamp } \mathsf{B}_a, \qquad \text{new}(\overline{\underline{0}}) \notin \underline{a}.$$

The argument $\overline{0}$ in the oracle $\text{new}(-)$ intends to emphasize that the choice of the new symbol should be made based on the primary local state.

Operation $\mathrm{Init}()$:
$$(\underline{a}^k)' = \mathbf{0},$$
$$(\boxed{a}^k)' = \langle\mathbf{0}\rangle.$$

Operation $\mathrm{Upd}(0)$:
$$(\underline{0}^0)' = \mathrm{new}(\boxed{0}),$$
$$(\boxed{0}^0)' = \mathrm{new}(\boxed{0}) \cdot \boxed{0}^0_{|(\underline{0})'}.$$

Operation $\mathrm{Sync}(a, b)$:
$$(\underline{a}^k)' = (\underline{b}^k)' = \begin{cases} \underline{a}^a \underset{ab}{\sqcup} \underline{b}^b & \text{if } k \in \{a, b\}, \\ \underline{a}^k \underset{ab}{\sqcup} \underline{b}^k & \text{otherwise,} \end{cases}$$

if $k \in \{a, b\}$:
$$(\boxed{a}^k)' = (\boxed{b}^k)' = \begin{cases} \boxed{b}^b_{|(\underline{b})'} & \text{if } \underline{a}^a \in \underline{b}, \\ \boxed{a}^a_{|(\underline{a})'} & \text{otherwise,} \end{cases}$$

if $k \neq a$ and $k \neq b$:
$$(\boxed{a}^k)' = \begin{cases} \boxed{b}^k & \text{if } (\underline{a}^k)' \neq \underline{a}^k, \\ \boxed{a}^k & \text{otherwise,} \end{cases}$$
$$(\boxed{b}^k)' = \begin{cases} \boxed{a}^k & \text{if } (\underline{b}^k)' \neq \underline{b}^k, \\ \boxed{b}^k & \text{otherwise.} \end{cases}$$

Fig. 7. Semantics of operations on BS model.

Data causality ordering under the Bounded Stamps model is defined by

$$\mathsf{B}_a \leq_\mathsf{B} \mathsf{B}_b \quad \text{iff} \quad \underline{a}^a \in \underline{b}.$$

The correctness of the proposed model follows from the observation that, apart from the cached orders used for the symbol reuse mechanism, it is actually an encoding of the CMPV model. To formalize the correspondence between both models, we introduce an encoding function $[\![-]\!]_-$ that maps each integer in the CMPV model into the corresponding symbol (in $\mathcal{L}_N$) in the state resulting from a given trace. This map is defined recursively on the traces.

$$\begin{aligned} [\![n]\!]_{\mathrm{Init}()} &= \mathbf{0}, \\ [\![n]\!]_{\alpha \cdot \mathrm{Upd}(0)} &= \begin{cases} \mathrm{new}(\boxed{0}_\alpha) & \text{if } n = \big|\alpha_{|\mathrm{Upd}(0)}\big| + 1, \\ [\![n]\!]_\alpha & \text{otherwise,} \end{cases} \\ [\![n]\!]_{\alpha \cdot \mathrm{Sync}(x,y)} &= [\![n]\!]_\alpha. \end{aligned}$$

Where $\big|\alpha_{|\mathrm{Upd}(0)}\big|$ is the number of update events in α, $\boxed{0}_\alpha$ is the bounded stamp for the primary after trace α, and $\mathrm{new}(\boxed{0}_\alpha)$ gives a canonical choice for the new principal element on the primary after the update. When we discard the cached orders, the semantics of operations given in Fig. 7 are precisely the ones in CMPV (Figure 6) affected by the encoding map. Moreover, the principal orders are encodings for the restricted orders presented in the previous section.

Lemma 2. *For an arbitrary trace α, replicas index a and b:*

1. $\underline{a}^k = [\![a^k]\!]_\alpha$,
2. $[\![a^i]\!]_\alpha = [\![a^j]\!]_\alpha$ *implies* $a^i = a^j$,
3. $x \leq^a y$ *iff* $[\![x]\!]_\alpha \leq_B^a [\![y]\!]_\alpha$.

Proof. This results from a simple induction on the length of traces. When the last operation was $\mathrm{Init}()$ it is trivial. When it was $\mathrm{Upd}(0)$, the result follows from the induction hypothesis and the axiom for the oracle $\mathrm{new}(-)$. When it was $\mathrm{Sync}(x, y)$ the result follows from induction hypothesis, the fact that definitions on both models coincide since $\leq^{ab}$ computes the required joins (Proposition 3), and the correctness of the new restricted orders (Proposition 4).

As a simple consequence of the previous result, we can state the following correctness result.

Proposition 5. *For any arbitrary trace α and replica indexes a and b we have*

$$\mathsf{B}_a \leq_B \mathsf{B}_b \quad \textit{iff} \quad \mathsf{C}_a \leq_C \mathsf{C}_b.$$

Proof. Immediate from Lemma 2 and the definitions of $\leq_B$ and $\leq_C$.

It remains to instantiate the parameters of the model. A trivial but unbounded instantiation would be: set $\mathcal{L}_N$ as the integers, $\mathbf{0}$ as value 0 and $\mathrm{new}(\boxed{0}) = \underline{0}^0 + 1$. In this setting, principal orders would be an explicit representation of counter mode restricted orders. Obviously, we are interested in bounded instantiations of $\mathcal{L}_N$. To show that such instantiations exists, we introduce the following lemma that puts in evidence the role of cached orders.

Lemma 3. *For every stamp B_a there exists an i such that*

$$\boxed{a}^a \subseteq \boxed{0}^i.$$

Proof. See [1].

We are now able to present a bounded instantiation for the model. Let $\mathcal{L}_N$ be a totally ordered set with N^2 elements (the total order is here only to avoid making non-deterministic choices). We define:

$$\mathbf{0} = \sqcap \mathcal{L}_N,$$
$$\mathrm{new}(\boxed{a}) = \sqcap\{x \mid x \in \mathcal{L}_N \text{ and } x \notin \boxed{a}\}.$$

Lemma 3 guarantees that $\mathrm{new}(\boxed{0})$ satisfies the axiom. It follows then that it acts as an encoding of counter mode model (Proposition 5). Thus we have constructed a bounded model for the data causality problem in a slice, which generalizes, by concatenating slices, to the full data causality problem addressed by version vectors.

6 Related Work

On what concerns bounded replacements for version vectors there is, up to our knowledge, no previous solution to the problem. The possible existence of a bounded substitute to version vectors was referred in [2] while introducing the version stamps concept. Version stamps allow the characterization of data causality in settings where version vectors cannot operate, namely when replicas can be created and terminated autonomously.

There have been several approaches to version vector compression. Update coalescing [15] takes advantage of the fact that several consecutive updates issued in isolation in a single replica can be made equivalent to a single large update. Update coalescing is intrinsic in bounded stamps since sequence restriction in the update operation discards non-propagated symbols. Dynamic compression [15] can effectively reduce the size of version vectors by removing a common minimum from all entries (along each slice). However, this technique requires distributed consensus on all replicas and therefore cannot progress if one or more replicas are unreachable. Unilateral version vector pruning [17] avoids distributed consensus by allowing unilateral deletion of inactive version vectors entries, but relies on some timing assumptions on the physical-clock's skew.

Lightweight version vectors [9] develop an integer encoding technique that allows a gradual increase of integer storage as counters increase. This technique is used in conjunction with update coalescing to provide a dynamic size representation. Hash histories [10] track data causality by collecting hash fingerprints of contents. This representation is independent of the number of replicas but grows in proportion to the number of updates.

The minimality of vectors clocks as a characterization of Lamport causality [12], presented by Charron-Bost [4] and recently re-addressed in [7], indicates particular runs where the full expressiveness of vectors clocks is required. However there are cases in which smaller representations can operate: Plausible Clocks [20] offer a bounded substitute to vectors clocks that are accurate in a large percentage of situations and may be used in settings were deviations only impacts performance and not correctness; Resettable Vector Clocks [3] allow a bounded implementation of vector clocks under a specific communication pattern between processes.

The collection of cached copies of the knowledge in other replicas has been explored before in [6, 21] and used for optimization of message passing strategies. This concept is sometimes referred to as matrix clocks [16]. These clocks are based on integer counters and are similar to our intermediate "counter mode principal vector" representation.

7 Conclusions

Version vectors are the key mechanism in the detection of inconsistency and obsolescence among optimistically replicated data. This mechanism has been used extensively in the design of distributed file systems [11, 8], in particular for data causality tracking among file copies. It is well known that version vectors are unbounded due to their use of counters; some approaches in the literature have tried to address this problem.

We have brought the attention to the fact that causally ordering a limited number of replicas does not require the full expressive power of version vectors. Due to the limited number of configurations among replicas, data causality tracking does not necessarily imply the use of unbounded mechanisms. As a consequence, Charron-Bost's minimality of vector clocks cannot be transposed to version vectors.

We have noted that to find a bounded alternative to version vectors, it was enough to concentrate on a sub-problem: keeping distributed knowledge about a total order generated by a single entity.

The key to bounded stamps was defining an intermediate unbounded mechanism and showing that it was possible to perform comparisons without requiring a global total order; this was the bulk of the proof correctness; bounded stamps were then derived as an encoding into a finite set of symbols. This required the definition of a non-trivial symbol reuse mechanism that is able to progress even if an arbitrary number of replicas ceases to participate in the exchanges. This mechanism may have a broader applicability beyond its current use (e.g. log dissemination and pruning) and become a building block in other mechanisms for distributed systems.

The construction of the mechanism was supported by a simulator[1], which was used in the proof of correctness so as to probe (and discard) tentative hypotheses. The simulator was also turned into a model checker which proved the correctness up to $N = 4$, giving some confidence before the full proof of correctness was attempted.

Bounded version vectors are obtained by substituting integer counters on version vectors by bounded stamps. It represents the first bounded mechanism for detection of obsolescence and mutual inconsistency in distributed systems.

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A   Proof of Lemma 1

The hypothesis of Lemma 1 concern two stamps (say a and b) in which we can identify some sort of conflict between each stamp knowledge: Stamp b has a better knowledge concerning the primary state ($a^a \leq b^b$) but has an outdated vision concerning some other stamp (say k), i.e. $b^k \leq a^k$. Lemma 1 states that when this happens stamp b already attributes the value of a^k to some other stamp (say l — that is, $b^l = a^k$). In order to prove this result, it will be necessary to reinforce this statement: not only $b^l = a^k$ but it is possible to identify a flow of information between stamp l and k. Moreover, this flow of information (a sequence of synchronization operations starting from l to k) can be traced in stamp b's local state as a sequence of indexes enjoying some properties. These sequence of indexes are called *delay paths* and are defined as follows.

Definition 1 (Delay Path). *A* delay path *between* a^k *and* b *is a non-empty sequence of indexes* $\langle i_0, \ldots, i_n \rangle$ *such that, for any stamp* c,

1. $i_0 = k$,
2. $\mathsf{b}^{i_n} = \mathsf{a}^k$,
3. $\mathsf{b}^{i_p} < \mathsf{a}^k$ *for all* $0 \leq p < n$,
4. $\mathsf{a}^k \leq \mathsf{i_p}^{i_{p-1}}$ *for all* $0 < p \leq n$,
5. $\mathsf{a}^k < \mathsf{c}^{i_p} \Rightarrow \mathsf{a}^k \leq \mathsf{c}^{i_{p-1}}$ *for all* $0 < p \leq n$.

    Some simple facts concerning delay paths.

Proposition 6. *Let* $\langle i_0, \ldots, i_n \rangle$ *be a delay path between* a^k *and* b. *The following facts hold:*

1. $\mathsf{b}^k \leq \mathsf{a}^k$,
2. $\mathsf{a}^k \in \mathsf{b}$,
3. $\mathsf{a}^k \leq \mathsf{i_p}^{i_p}$ *for all* $0 \leq p \leq n$,
4. $\mathsf{b} \in \langle i_0, \ldots, i_n \rangle \Rightarrow n = 0$.

Proof. The first three facts are immediate consequences from the definition and Proposition 1. Regarding the last fact, if b occurred in a position i_p, being $n > 0$, by condition (4) of delay paths we have $\mathsf{a}^k \leq \mathsf{b}^{i_{p-1}}$; but this contradicts condition (3). Thus, b only occurs in a singleton delay path.

    Some of the conditions on delay paths impose global constrains on them that will allow to reason about global state changes and their impact on the local states. The following Lemma exposes the use of such global constrains.

Lemma 4 (Pointwise-join Lemma). *Let* $\langle i_0, \ldots, i_n \rangle$ *be a non-empty sequence of indexes. If for some* x,

1. $\mathsf{b}^{i_n} = x$,
2. *for all* $0 \leq p < n$, $\mathsf{b}^{i_p} < x$,
3. *for all* $0 < p \leq n$ *and any stamp* c, *if* $x < \mathsf{c}^{i_p}$ *then* $x \leq \mathsf{c}^{i_{p-1}}$.

Then, for any stamp d *for which* $\mathsf{d}^{i_0} \leq x$, *there exists* $0 \leq q \leq n$ *such that* $\mathsf{b}^{i_q} \sqcup \mathsf{d}^{i_q} = x$ *and, for all* $0 \leq p < q$, $\mathsf{b}^{i_p} \sqcup \mathsf{d}^{i_p} < x$.

Proof. By induction on the length of the sequence $\langle i_0, \ldots, i_n \rangle$. For the base case (singular sequence) we have that $\mathsf{b}^{i_0} = x$. Since $\mathsf{d}^{i_0} \leq x$ we have $\mathsf{b}^{i_0} \sqcup \mathsf{d}^{i_0} = x$ and the remaining condition is vacuous. For the induction step, we consider the following cases: If $\mathsf{d}^{i_0} = x$ then we set $q = 0$ since $\mathsf{b}^{i_0} \sqcup \mathsf{d}^{i_0} = x$. Otherwise, we know that $\mathsf{b}^{i_0} \sqcup \mathsf{d}^{i_0} < x$ and, by (4), that $\mathsf{d}^{i_1} \leq x$. So we apply the induction hypothesis to the sequence $\langle i_1, \ldots, i_n \rangle$ and set q to the resulting index plus 1.

    We now show that the conditions in Lemma 1 are sufficient to establish the existence of delay paths.

Lemma 5. *If* a *and* b *are two stamps and* k *a position such that*

$$\mathsf{a}^a \leq \mathsf{b}^b \quad and \quad \mathsf{b}^k \leq \mathsf{a}^k,$$

then there exists a delay path between a^k *and* b.

Proof. We prove by induction on the length of the trace. If the last operation was $\mathrm{Init}()$ we use the singleton sequence $\langle k \rangle$ for the delay path and the conditions hold trivially. If the last operation was $\mathrm{Upd}(0)$ consider the following cases:

$a = b = 0$: we pick the sequence $\langle k \rangle$ that satisfies trivially all conditions;

$a = 0 \neq b$: after the update $\mathsf{a}^a \not\leq \mathsf{b}^b$, which contradicts the hypothesis;

$b = 0 \neq a$: if $k = 0$ then $\mathsf{b}^k \not\leq \mathsf{a}^k$, which contradicts the hypothesis. If $k \neq 0$ we use the same delay path that comes from the induction hypothesis, which is still valid after the update because it does not contain position 0, since $b = 0 \neq k$ (Proposition 6).

$0 \notin \{a, b\}$: we use the same delay path from the induction hypothesis, which is still valid: (1,2,3) because a and b are not affected by the update; (4) because only 0^0 changes; (5) because even if for some p we have $i_p = 0$, if $\mathsf{a}^k < 0^0$, then $\mathsf{a}^k \leq 0^{i_{p-1}}$ due to (4).

If the last operation was $\mathrm{Sync}(x, y)$ (and lets assume, without loss of generality, that y is the more up-to-date stamp, i.e. $\mathsf{x}^x \leq \mathsf{y}^y$) we need to distinguish the following cases:

$\{x, y\} \cap \{a, b\} = \emptyset$: we use the same delay path from the induction hypothesis, which is still valid: (1,2,3) because a and b are not affected; (4) because $\mathsf{i}_\mathsf{p}^{i_{p-1}}$ can only increase; (5) because for every $\mathsf{c} = \mathsf{x} \sqcup \mathsf{y}$, if $\mathsf{a}^k < \mathsf{c}^{i_p}$, then either c^{i_p} is computed pointwise and $\mathsf{a}^k \leq \mathsf{c}^{i_{p-1}}$ follows from the induction hypothesis, or i_p is either x or y and (by 4) $\mathsf{a}^k \leq \mathsf{i}_\mathsf{p}^{i_{p-1}} \leq \mathsf{c}^{i_{p-1}}$.

$\{x, y\} = \{a, b\}$: stamps a and b become equal after the synchronization and we pick the sequence $\langle k \rangle$ for the delay path;

$\{x, y\} \cap \{a, b\} = \{a\} \neq \{b\}$: in this case the stamp a results from the synchronization of x and y and we have $\mathsf{x}^x \leq \mathsf{y}^y = \mathsf{a}^a \leq \mathsf{b}^b$. Consider the following two cases:

When $k = x$ and $\mathsf{x}^x < \mathsf{y}^y = \mathsf{a}^k$. First, given that $\mathsf{y}^y \leq \mathsf{b}^b$ and $\mathsf{b}^y \leq \mathsf{y}^y$, we can apply the induction hypothesis to y and b on index y and establish the existence of a delay path $\langle i_0 = y, \ldots, i_n \rangle$ for y^y in b. Then we prefix it by k, obtaining $\langle k, y, \ldots, i_n \rangle$, which is a suitable delay path between a^k and b, given that: (1) holds by construction, (2) from the induction hypothesis, (3) from the induction hypothesis and $\mathsf{b}^k < \mathsf{a}^k$ (since $\mathsf{b}^k \leq \mathsf{x}^x < \mathsf{y}^y = \mathsf{a}^k$); (4) from the induction hypothesis and $\mathsf{a}^k = \mathsf{y}^y = \mathsf{y}^x = \mathsf{y}^k$; (5) from the induction hypothesis and because for every stamp c, $\mathsf{c}^y \leq \mathsf{y}^y = \mathsf{a}^a = \mathsf{a}^k$.

Otherwise, then either $\mathsf{a}^k = \mathsf{x}^k$ or $\mathsf{a}^k = \mathsf{y}^k$; applying the induction hypothesis to either x^k or y^k and b in position k gives us a valid delay path for the resulting configuration (all conditions hold, including (5) as shown for the case $\{x, y\} \cap \{a, b\} = \emptyset$).

$\{x, y\} \cap \{a, b\} = \{b\} \neq \{a\}$: in this case the stamp b results from the synchronization of x and y.

When k is either x or y, we have $b^k = b^b = y^y$; but this means (as $a^a \leq b^b$ and $b^k \leq a^k$) that $a^k = b^k$; therefore $\langle k \rangle$ is a delay path.

Otherwise, $b^k = x^k \sqcup y^k$; this means that $y^k \leq b^k \leq a^k$ and by the induction hypothesis there exists a delay path P between a^k and y. Given that also $x^k \leq a^k$, Lemma 4 establishes the existence of a sequence $Q = \langle i_0, \ldots, i_q \rangle$ (prefix of P) that is a delay path between a^k and b for the following reasons. Positions x and y do not appear in Q — $x, y \neq i_0$ because we are assuming $k \neq x, y$, and $x, y \neq i_p$ for $p > 0$, otherwise we would have $a^k \leq x^{i_{p-1}}, y^{i_{p-1}}$ (condition (4) of delay paths of which Q is a prefix) and then $a^k \leq x^{i_{p-1}} \sqcup y^{i_{p-1}}$, which contradicts Lemma 4. Thus, all elements b^j, with $j \in Q$ are computed pointwise (i.e $b^j = x^j \sqcup y^j$), making conditions (2,3 and 5) immediate consequences of Lemma 4. Condition (1) is trivially observed (Q is a prefix of P); and condition (4) from the induction hypothesis and because upon a join values can only increase.

We can finally state Lemma 1.

Lemma (1). For every stamp C_a and C_b, and every index k,

$$a^a \leq b^b \text{ and } b^k \leq a^k \quad \text{implies} \quad a^k \in b.$$

Proof. Direct from Lemma 5.

B Proof of Lemma 3

Lemma 3 says that each principal order is already contained in some cached order on the primary. Note that Lemma 1 already states that every principal element a^a belongs to the primary principal vector, and delay paths were used to show where it can be found. Now, we will show that it is precisely in the primary cached order located in the position pointed out by the delay path between a^a and 0 that we can find all the elements in $\boxed{a}^a$. To prove this we need to reason about cached orders along delay paths. This suggests an extension of these to what we call *principal delay paths*.

Definition 2. *A principal delay path for stamp B_a is a delay path $\langle i_0, \ldots, i_n \rangle$ between a^a and 0 that additionally satisfies the following condition: for every $0 \leq p \leq n$ and any stamp B_c,*

$$a^a = c^{i_p} \quad implies \quad \boxed{a}^a \subseteq \boxed{c}^{i_p} \text{ or}$$
$$(p > 0 \text{ and } a^a \leq c^{i_{p-1}}).$$

We now prove the existence of principal delay paths by extending the proof of existence in Lemma 5. Here we only go through the cases that are relevant for the additional condition.

Lemma 6. *For every stamp B_a there exists a principal delay path.*

Proof. (Sketch)

Consider the following additional arguments to the proof of Lemma 5. If the last operation was $\mathrm{Sync}(x, y)$ (assume $x^x \leq y^y$):

$\{x, y\} \cap \{a, 0\} = \emptyset$: let $\mathsf{c} = \mathsf{x} \sqcup \mathsf{y}$. If i_p is either x or y, we know that $p > 0$ (since $a \notin \{x, y\}$). Let $\mathsf{c}^{i_p} = \mathsf{x}^x \sqcup \mathsf{y}^y = \mathsf{y}^y$. When $\mathsf{a}^a = \mathsf{c}^{i_p}$, by condition (4), we have $\mathsf{a}^a \leq \mathsf{x}^{i_{p-1}}$ or $\mathsf{a}^a \leq \mathsf{y}^{i_{p-1}}$ which determines that $\mathsf{a}^a \leq \mathsf{c}^{i_{p-1}}$. When c^{i_p} is computed pointwise, the new condition follows by the induction hypothesis.

$\{x, y\} \cap \{a, 0\} = \{a\} \neq \{0\}$: when $a = x$ and $\mathsf{x}^x < \mathsf{y}^y$, let $\langle i_0 = y, \ldots, i_n \rangle$ be the principal delay path for y. The new condition if verified for $\langle a, y, \ldots, i_n \rangle$ since, the case $c \neq y$ is trivial (because $\mathsf{c}^a = \mathsf{c}^x \leq \mathsf{x}^x < \mathsf{y}^y = \mathsf{a}^a$). For $c = y$, the new condition is satisfied since $\boxed{a}^a \subseteq \boxed{y}^y$ (Proposition 4).

$\{x, y\} \cap \{a, 0\} = \{0\} \neq \{a\}$ in this case the primary results from the synchronization of x and y (i.e. y is the primary before synchronization). Since $x \neq a$, then $\mathsf{0}^a$ is computed pointwise. By induction hypothesis we get a principal delay path P to which we apply Lemma 4 to get a new sequence Q where x and y never occur (c.f. proof of Lemma 5). The new condition follows by the induction hypothesis.

Lemma (3). For every stamp B_a there exists a position i such that

$$\boxed{a}^a \subseteq \boxed{0}^i.$$

Proof. Let $\langle i_0, \ldots, i_n \rangle$ be the principal delay path for a (given by Lemma 6). Instantiating the new condition for 0 on i_n we get that

$$\boxed{a}^a \subseteq \boxed{0}^{i_n}.$$